

## **IN THE CLAIMS**

Please amend the claims as follows:

1. (currently amended) A method for automatically altering the execution flow of  
a first computer program while the first program is executing, the method comprising the  
steps of:

~~establishing a breakpoint in the computer program;~~

~~initiating execution of the computer program;~~

~~interrupting execution of the computer program at an origin address where the  
breakpoint is detected; and~~

~~changing the instruction pointer to point to a destination address specified by the  
breakpoint; and~~

~~continuing execution of the computer program from the destination address;~~

executing the first program as a child of a second program, wherein instructions  
for the executing first program reside in respective memory locations of a computer and  
correspond to respective source code locations of the first program, and wherein the  
second program is operable for altering memory locations of the computer allocated to  
the child program, including memory designated for registers of the child program and  
memory designated for instructions of the child program;

receiving by the second program a desired flow of execution for the first program  
from a user predefined list, wherein the list includes a plurality of jump instructions, each  
jump instruction defining respective originating and destination source code locations in  
the first program; and

inserting the plurality of jump instructions into memory locations of the computer corresponding to respective originating source code locations of the first program defined by the respective jump instructions, wherein for each of the plurality of inserted jump instructions, execution flow in the first program jumps from the memory location corresponding to the respective jump instruction's originating source code location to the memory location corresponding to the respective jump instruction's defined destination source code location responsive to the first program encountering the memory location corresponding to the respective jump instruction's originating source code location, wherein the inserting is performed automatically by the second program responsive to the user predefined list.

2. (currently amended) The method as claimed in claim 1, wherein each jump instruction also defines a location for temporary storage of one or more instructions corresponding to the respective jump instruction's originating source code location of the first program, the method further comprising the step of temporarily storing, for each of the plurality of inserted jump instructions, an original instruction of the first program corresponding to the respective jump instruction's originating source code location.

3. (canceled)

4. (currently amended) The method as claimed in claim 2, ~~3~~, further comprising the step of restoring the temporarily stored original instruction at the origin address after the step of inserting ~~interrupting execution of the computer program and before the step of changing the instruction pointer.~~

5. (canceled)

7. (currently amended) The method as claimed in claim 1, wherein for each of the plurality of inserted jump instructions, the method further comprises~~ing~~ the step of adding the memory location corresponding to the respective jump instruction's originating source code location ~~breakpoint address~~ to a debugging register.

8. (currently amended) The method as claimed in claim 7, wherein for each of the plurality of inserted jump instructions, the method further comprises the step of removing the memory location corresponding to the respective jump instruction's originating source code location breakpoint address from a debugging register.

9. (currently amended) The method as claimed in claim 1, wherein for each of the plurality of inserted jump instructions, the method further comprises the step of clearing the jump instruction breakpoint from the computer program.

10-11. (canceled)

12. (currently amended) The method as claimed in claim 1, further comprising the step of loading the first computer program as a child process of the second program debugger application.

13-14. (canceled)

15. (currently amended) A computer program product, recorded on a medium, for automatically altering the execution flow of a first computer program while the first program is executing, the computer program product comprising software for performing the steps of:

~~establishing a breakpoint in the computer program;~~

~~initiating execution of the computer program;~~

~~interrupting execution of the computer program at an origin address where the breakpoint is detected; and~~

~~changing the instruction pointer to point to a destination address specified by the breakpoint; and~~

~~continuing execution of the computer program from the destination address.~~

executing the first program as a child of a second program, wherein instructions for the executing first program reside in respective memory locations of a computer and correspond to respective source code locations of the first program, and wherein the second program is operable for altering memory locations of the computer allocated to the child program, including memory designated for registers of the child program and memory designated for instructions of the child program;

receiving by the second program a desired flow of execution for the first program from a user predefined list, wherein the list includes a plurality of jump instructions, each jump instruction defining respective originating and destination source code locations in the first program; and

inserting the plurality of jump instructions into memory locations of the computer corresponding to respective originating source code locations of the first program defined by the respective jump instructions, wherein for each of the plurality of inserted jump instructions, execution flow in the first program jumps from the memory location corresponding to the respective jump instruction's originating source code location to the memory location corresponding to the respective jump instruction's defined destination source code location responsive to the first program encountering the memory location corresponding to the respective jump instruction's originating source code location, wherein the inserting is performed automatically by the second program responsive to the user predefined list.

16. (currently amended) A computer system for automatically altering the execution flow of a first computer program while the first program is executing, the computer system executing computer software for performing the steps of:

~~establishing a breakpoint in the computer program;~~

~~initiating execution of the computer program;~~

~~interrupting execution of the computer program at an origin address where the breakpoint is detected; and~~

~~changing the instruction pointer to point to a destination address specified by the breakpoint; and~~

~~continuing execution of the computer program from the destination address;~~

executing the first program as a child of a second program, wherein instructions for the executing first program reside in respective memory locations of the computer system and correspond to respective source code locations of the first program, and wherein the second program is operable for altering memory locations of the computer allocated to the child program, including memory designated for registers of the child program and memory designated for instructions of the child program;

receiving by the second program a desired flow of execution for the first program from a user predefined list, wherein the list includes a plurality of jump instructions, each jump instruction defining respective originating and destination source code locations in the first program; and

inserting the plurality of jump instructions into memory locations of the computer corresponding to respective originating source code locations of the first program defined by the respective jump instructions, wherein for each of the plurality of inserted jump instructions, execution flow in the first program jumps from the memory location corresponding to the respective jump instruction's originating source code location to the memory location corresponding to the respective jump instruction's defined destination source code location responsive to the first program encountering the memory location

corresponding to the respective jump instruction's originating source code location, wherein the inserting is performed automatically by the second program responsive to the user predefined list.

17. (new) The computer program product as claimed in claim 15, wherein each jump instruction also defines a location for temporary storage of one or more instructions corresponding to the respective jump instruction's originating source code location of the first program, the computer program product further comprising software for performing the step of:

temporarily storing, for each of the plurality of inserted jump instructions, an original instruction of the first program corresponding to the respective jump instruction's originating source code location.

18. (new) The computer program product as claimed in claim 17, further comprising software for performing the step of:

restoring the temporarily stored original instruction at the origin address after the step of inserting.

19. (new) The computer program product as claimed in claim 15, wherein for each of the plurality of inserted jump instructions, the steps further comprise:

adding the memory location corresponding to the respective jump instruction's originating source code location to a debugging register.

20. (new) The computer program product as claimed in claim 19, wherein for each of the plurality of inserted jump instructions, the steps further comprise:

removing the memory location corresponding to the respective jump instruction's originating source code location from a debugging register.

21. (new) The computer program product as claimed in claim 15, wherein for each of the plurality of inserted jump instructions, the steps further comprise:

clearing the jump instruction from the computer program.

22. (new) The computer program product as claimed in 15, further comprising software for performing the step of:

loading the first computer program as a child process of the second program.

23. (new) The computer system as claimed in claim 16, wherein each jump instruction also defines a location for temporary storage of one or more instructions corresponding to the respective jump instruction's originating source code location of the first program, the computer system further executing computer software for performing the step of:

temporarily storing, for each of the plurality of inserted jump instructions, an original instruction of the first program corresponding to the respective jump instruction's originating source code location.

24. (new) The computer system as claimed in claim 23, the computer system further executing computer software for performing the step of:

restoring the temporarily stored original instruction at the origin address after the step of inserting.

25. (new) The computer system as claimed in claim 16, wherein for each of the plurality of inserted jump instructions, the computer system further executes computer software for performing the step of:

adding the memory location corresponding to the respective jump instruction's originating source code location to a debugging register.

26. (new) The computer system as claimed in claim 25, wherein for each of the plurality of inserted jump instructions, the computer system further executes computer software for performing the step of:

removing the memory location corresponding to the respective jump instruction's originating source code location from a debugging register.

27. (new) The computer system as claimed in claim 16, wherein for each of the plurality of inserted jump instructions, the computer system further executes computer software for performing the step of:

clearing the jump instruction from the computer program.